

Substitute for form 1449/PTO

Complete If Known

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

JAN 09 2006

Application Number	10/674,606
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Chan, Eddie P.
Art Unit	2183
Attorney Docket No.	AUS920030485US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
RF	AA 4,034,353	07-1977	Denny et al.	
	AB 4,821,178	04-1989	Levin et al.	
	AC 5,151,981	09-1992	Westcott et al.	
	AD 5,581,482	12-1996	Wiedenman et al.	
	AE 5,594,864	01-1997	Trauben	
	AF 5,691,920	11-1997	Levine et al.	
	AG 5,758,168	05-1998	Mealey et al.	
	AH 6,480,966	11-2002	Rawson, III	
	AI 2003/0135720	07-2003	DeWitt et al.	
	AJ 5,926,640	07-1999	Mason et al.	
	AK 6,185,652	02-2001	Shek et al.	
	AL 4,145,735	03-1979	Soga	
	AM 5,548,762	08-1996	Creedon et al.	
	AN 4,794,472	12-1988	Doyama	
	AO 6,161,187	12-2000	Mason et al.	
	AP 3,707,725	12-1972	Dellheim	
	AQ 6,408,386	06-2002	Hammond et al.	
	AR 6,775,728	08-2004	Zimmer et al.	
	AS 6,757,771	06-2004	Christie	
	AT 6,148,321	11-2000	Hammond	
↓	AU 6,275,893	08-2001	Bonola	
	AV 4,825,359	04-1989	Ohkami et al.	

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NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AW	Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 nd Ed., 1982, pp.434-443.
AX	Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner: /Ryan Fiegler/

Date Considered: 06/15/2006

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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RF	AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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RF	AA	5,710,881	Gupta et al.	
	AB	5,822,763	Baylor et al.	
	AC	5,928,334	Mandyam et al.	
	AD	6,094,709	Baylor et al.	
	AE	6,285,974 B1	Mandyam et al.	
	AF	6,636,950 B1	Mithal et al.	
	AG	2004/0205302 A1	Cantrill	
	AH	5,797,019	Levine et al.	
	AI	6,549,998 B1	Pekarich et al.	
	AJ	2003/0101367 A1	Bartfai et al.	
	AK	5,113,507	Jaekel	
	AL	6,681,387 B1	Hwu et al.	
	AM	6,925,424 B2	Jones et al.	

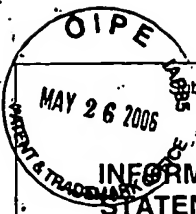
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NON-PATENT LITERATURE	
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RF	AN TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
	AO TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
	AP ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
	AQ Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
	AR "Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
	AS HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

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RF	AA 5051944	09-24-1991	Fetterolf et al.	
	AB 5142634	08-25-1992	Fite et al.	
	AC 5394529	02-28-1995	Brown, III et al.	
	AD 6233679 B1	05-15-2001	Holmberg	
	AE 5887159	03-23-1999	Burrows	
	AF 6662295 B2	12-09-2003	Yamaura	
	AG 5938760	08-17-1999	Levine et al.	
	AH 6189072 B1	02-13-2001	Levine et al.	
	AI 5276833	01-04-1994	Auvinen et al.	
	AJ 5287481	02-15-1994	Lin	
	AK 5537572	07-16-1996	Michelsen et al.	

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RF	AL JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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